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E. Willis  
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2815#

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

**Shigeru ATSUMI**

Serial No.: 09/028,276

Filed: February 24, 1998

For: Semiconductor Integrated Circuit Device and  
Flash EEPROM

Atty. Docket No.: 001701.73982

Group Art Unit: 2815

Examiner: J. Fenty

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(d)**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with Applicants' duty of disclosure, the following information is submitted for consideration by the United States Patent and Trademark Office in connection with the above-captioned application. The information is identified on the attached PTO 1449 form.

Applicants do not waive any right to take appropriate action to establish patentability over the listed documents should they be applied as references against the claims of the present application.

The undersigned certifies under 37 C.F.R. § 1.97(e)(1) that each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement. A copy of the Korean Office Action (with English translation) is also submitted herewith.

The requisite fee of \$180.00 under 37 C.F.R. § 1.17(p) to effect entry of this information disclosure statement should be charged to our Deposit Account No. 19-0733. Further, the Commissioner is authorized to charge any additional fees which may be required or credit any overpayment to our Deposit Account No. 19-0733.

It is respectfully requested that the Examiner fully consider each of the documents, initial the enclosed Form PTO-1449 in the appropriate place to indicate that the document has been

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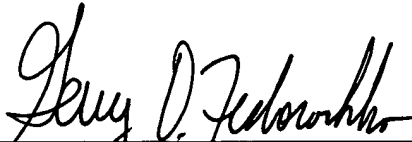
considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

Respectfully submitted,

BANNER & WITCOFF, LTD.

Dated: May 17, 2002

By:

  
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